

MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC BLOCK
STRUCTURE AND METHOD THEREFOR

Roy E. Scheuerlein et al.

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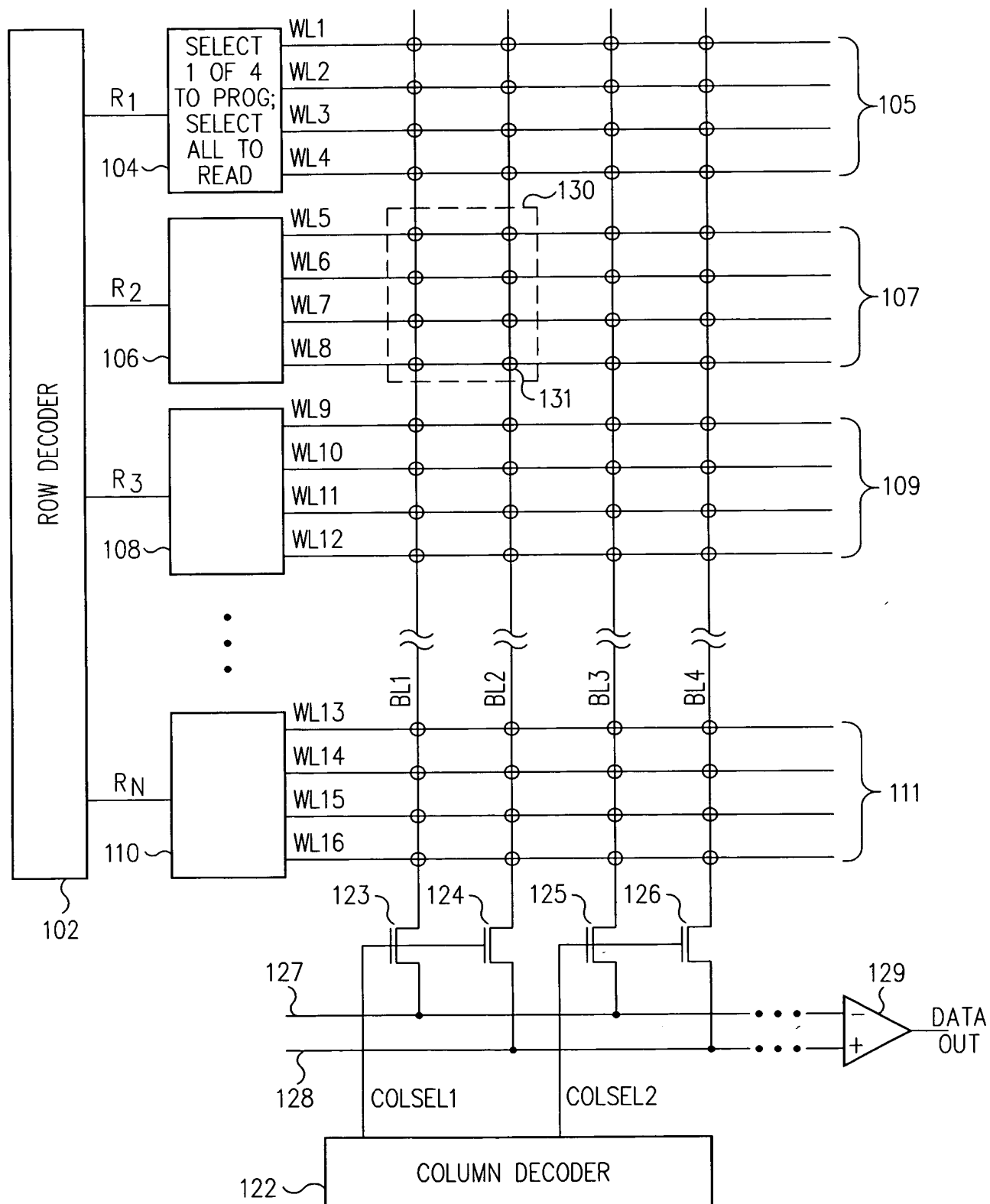
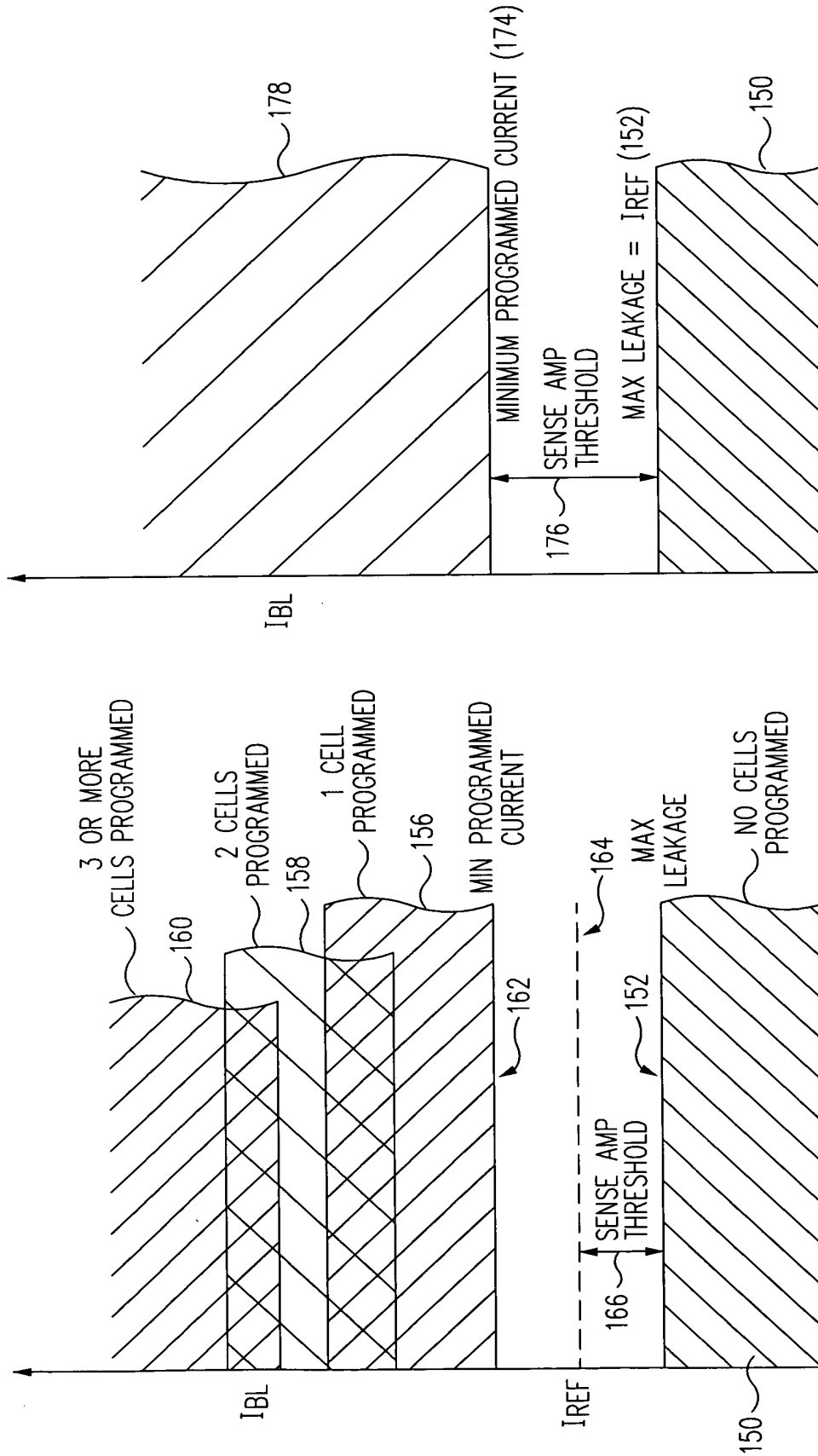
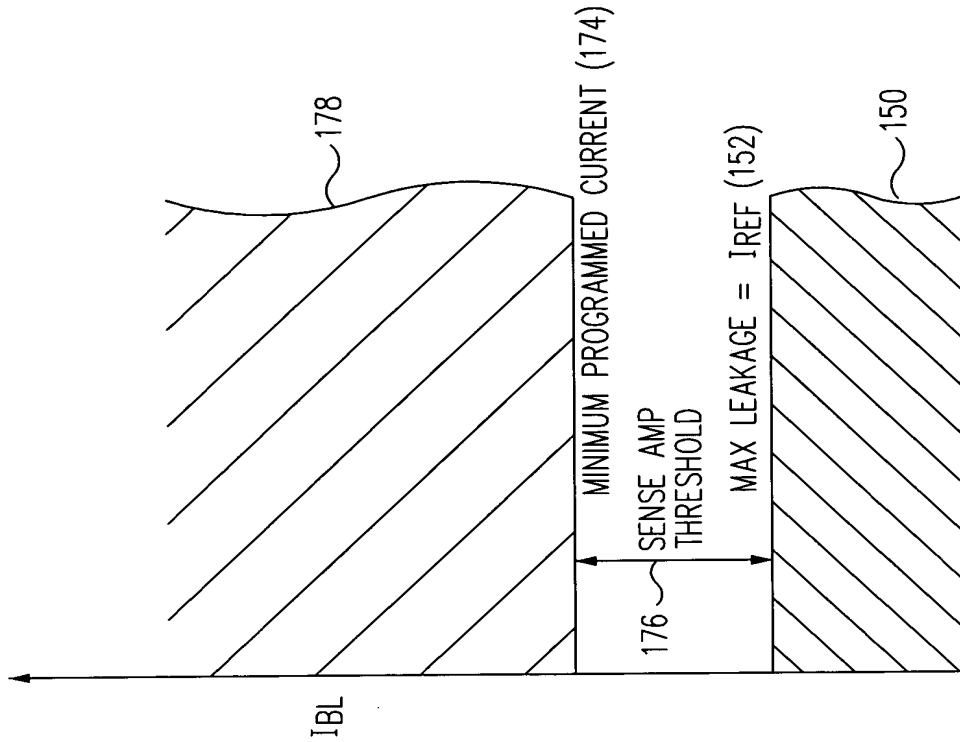


FIG. 1



BINARY DECISION
 WITH REFERENCE

FIG. 2



TWIN CELL

FIG. 3

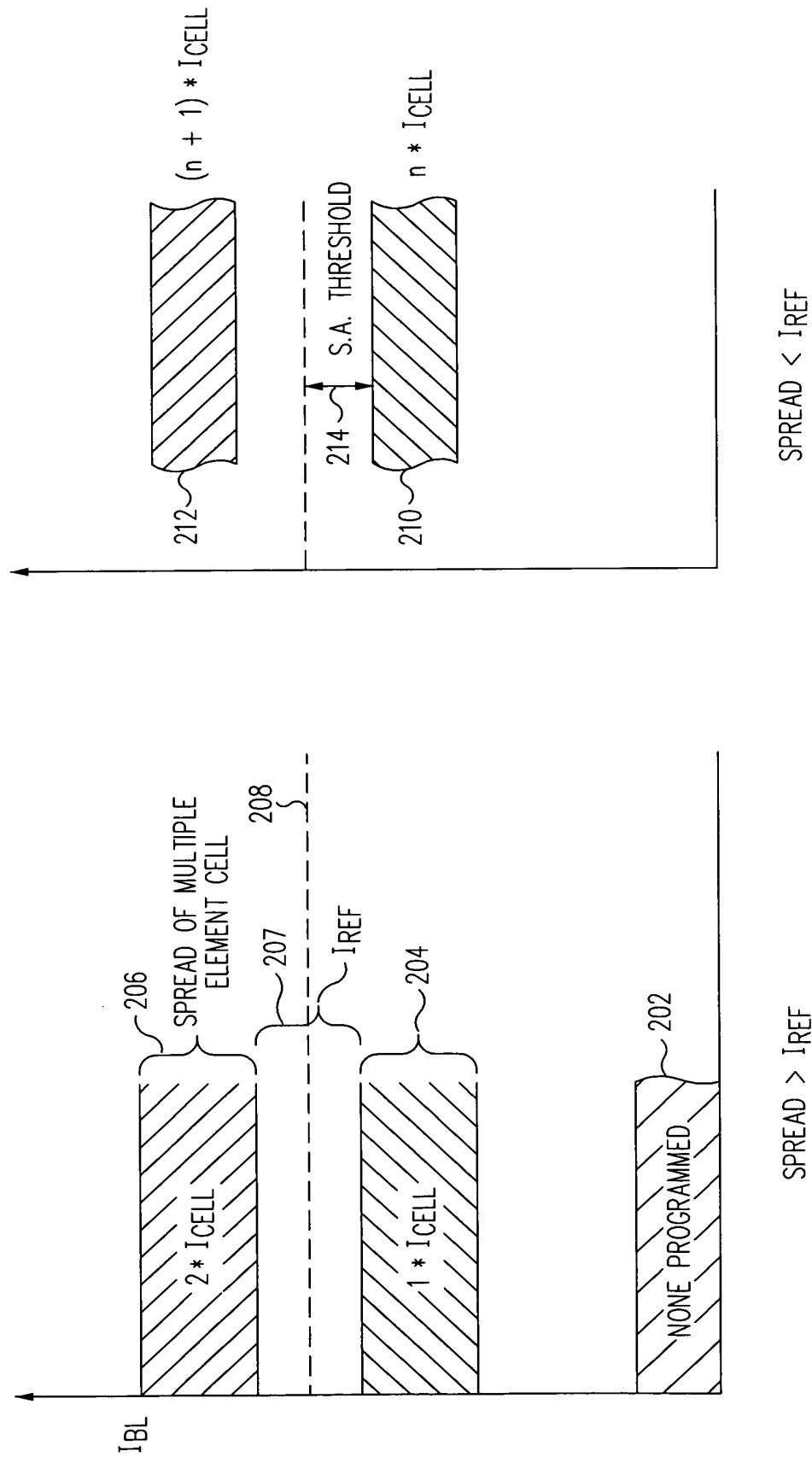


FIG. 4

FIG. 5

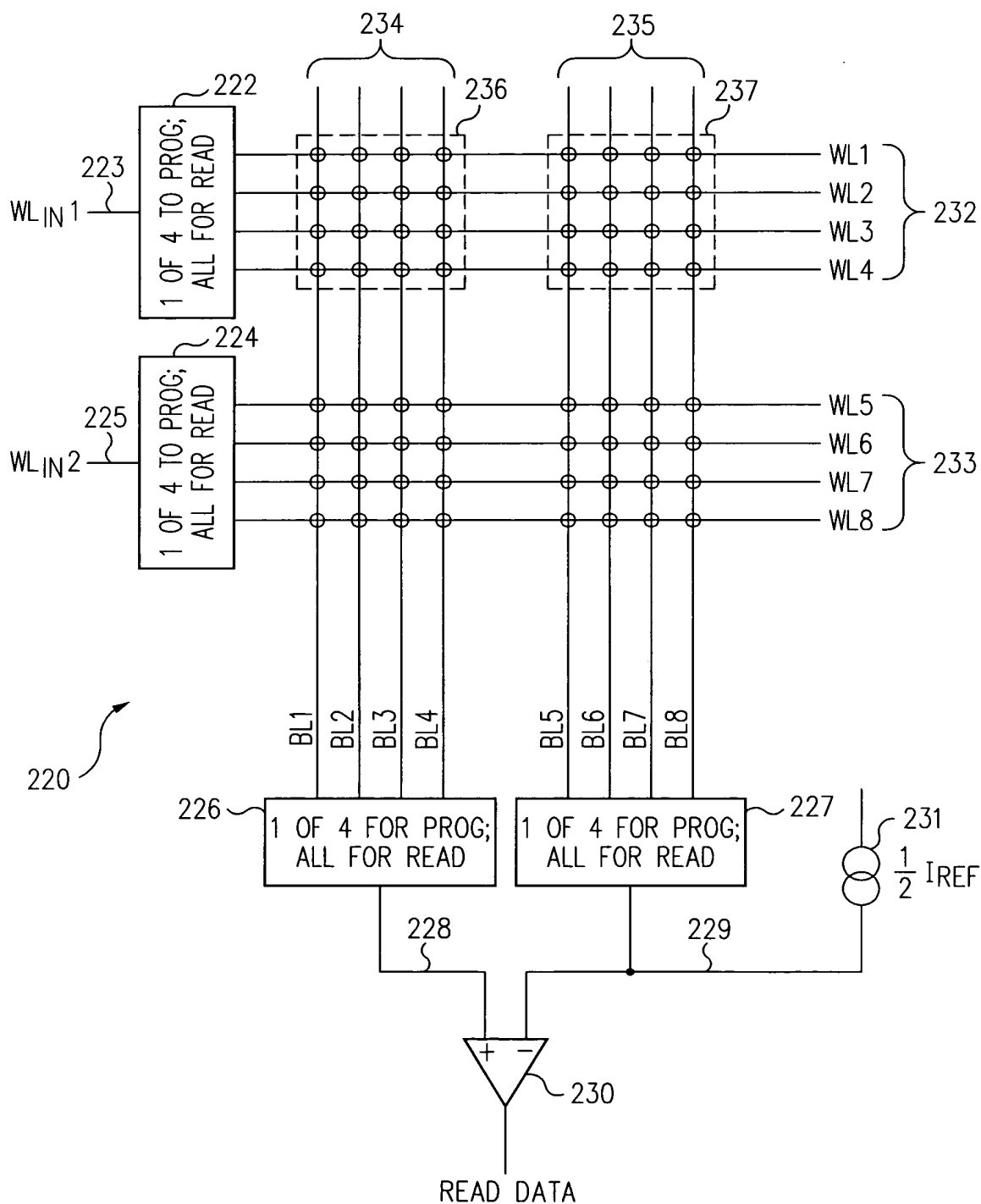


FIG. 6

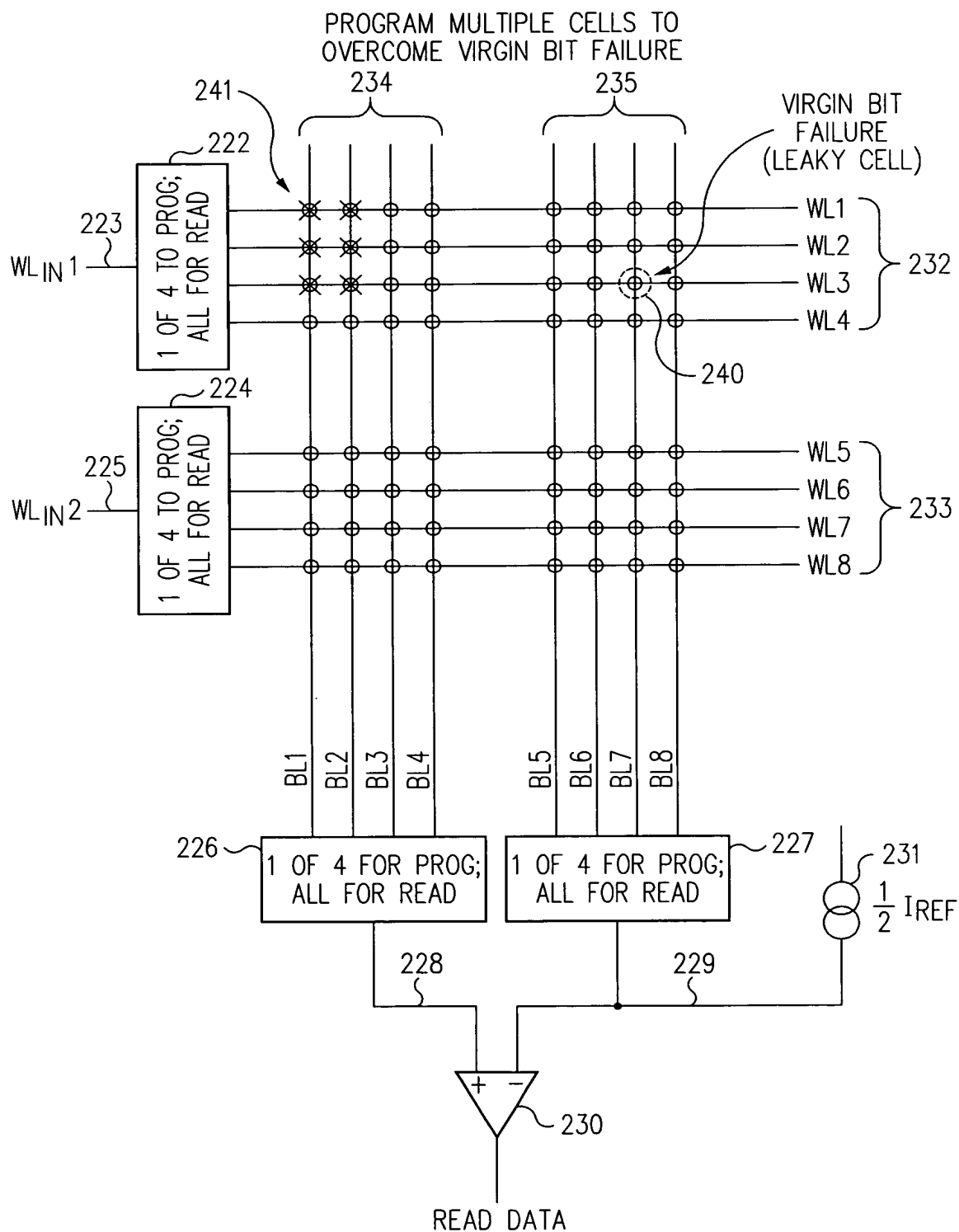


FIG. 7

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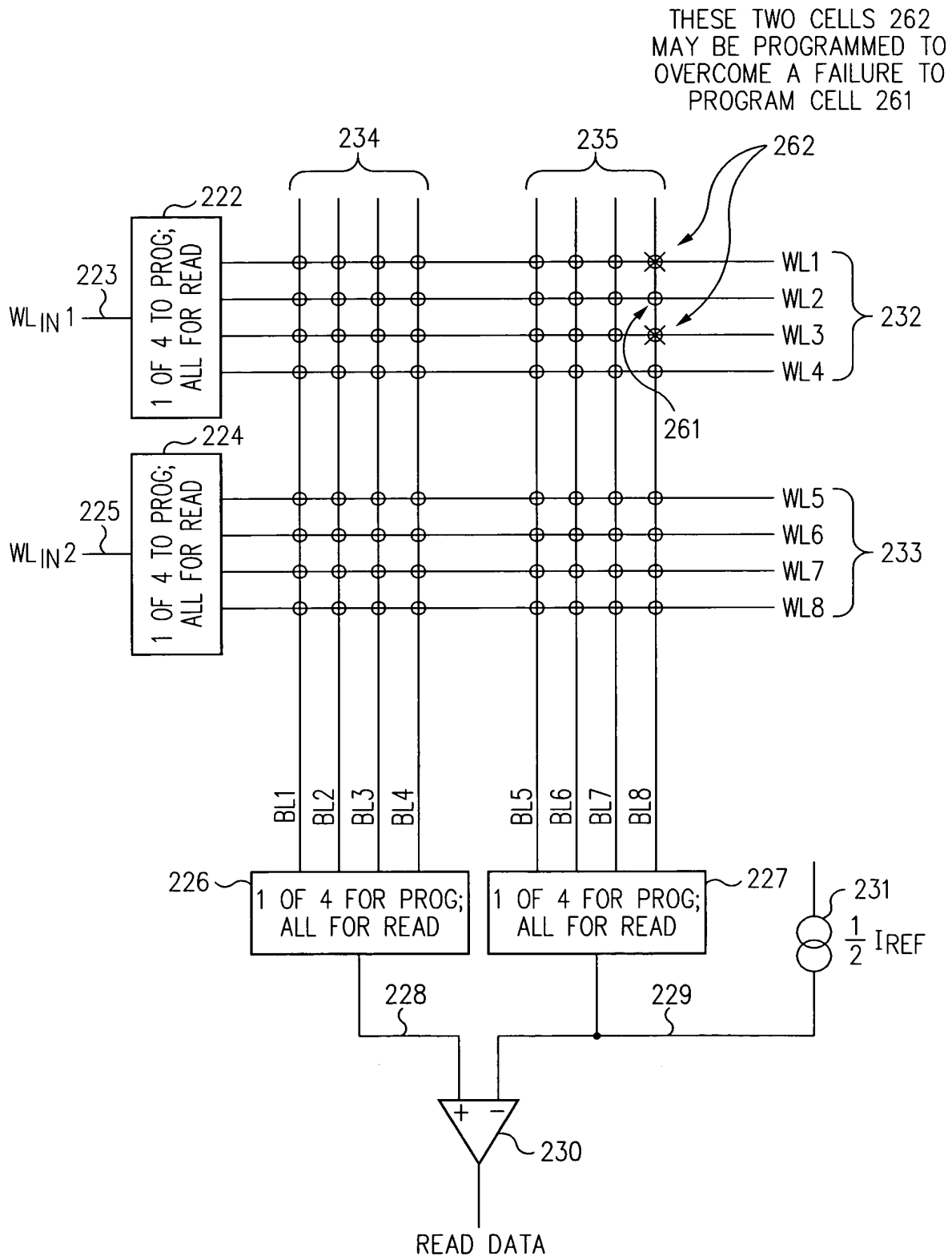


FIG. 8

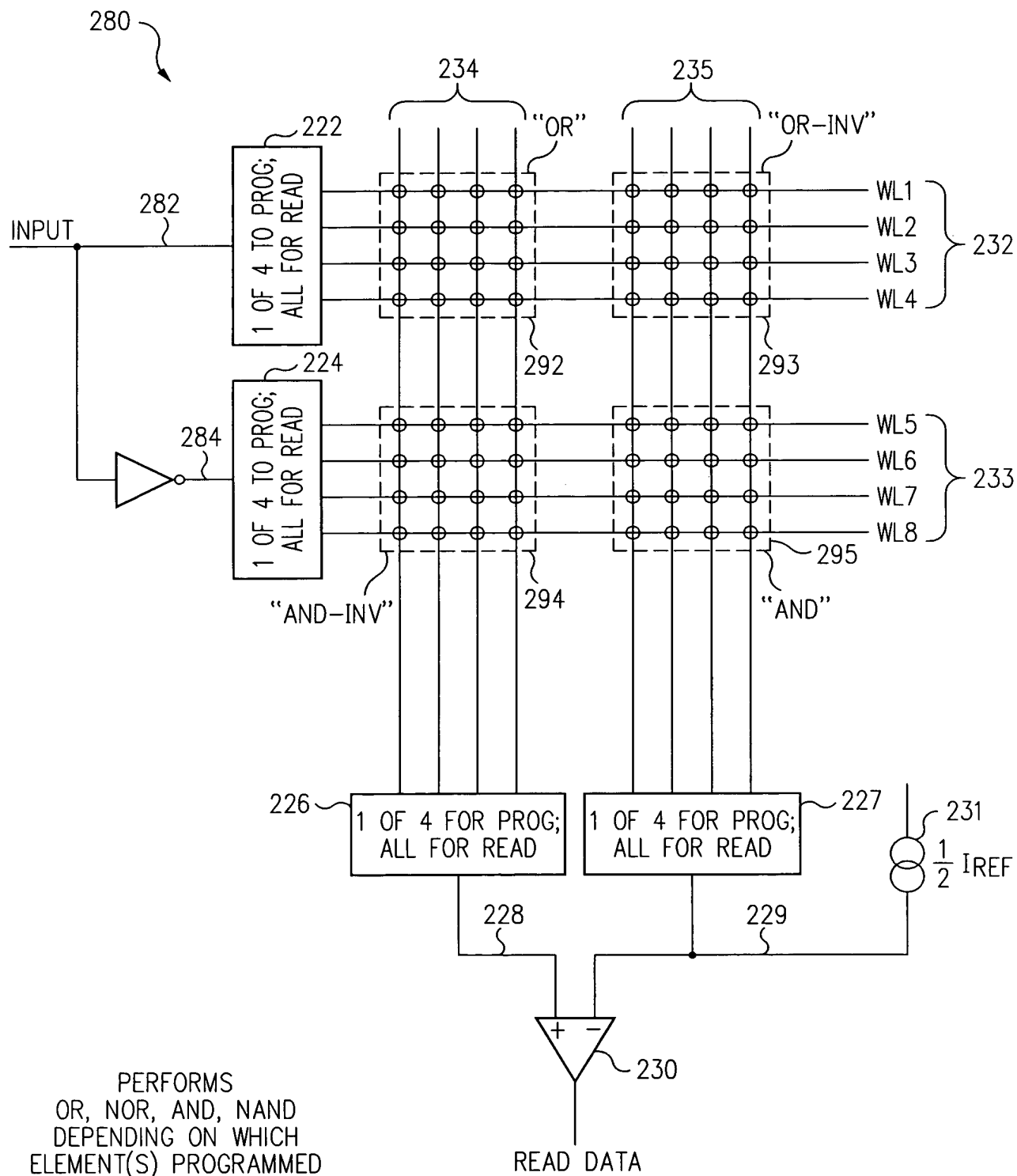


FIG. 9

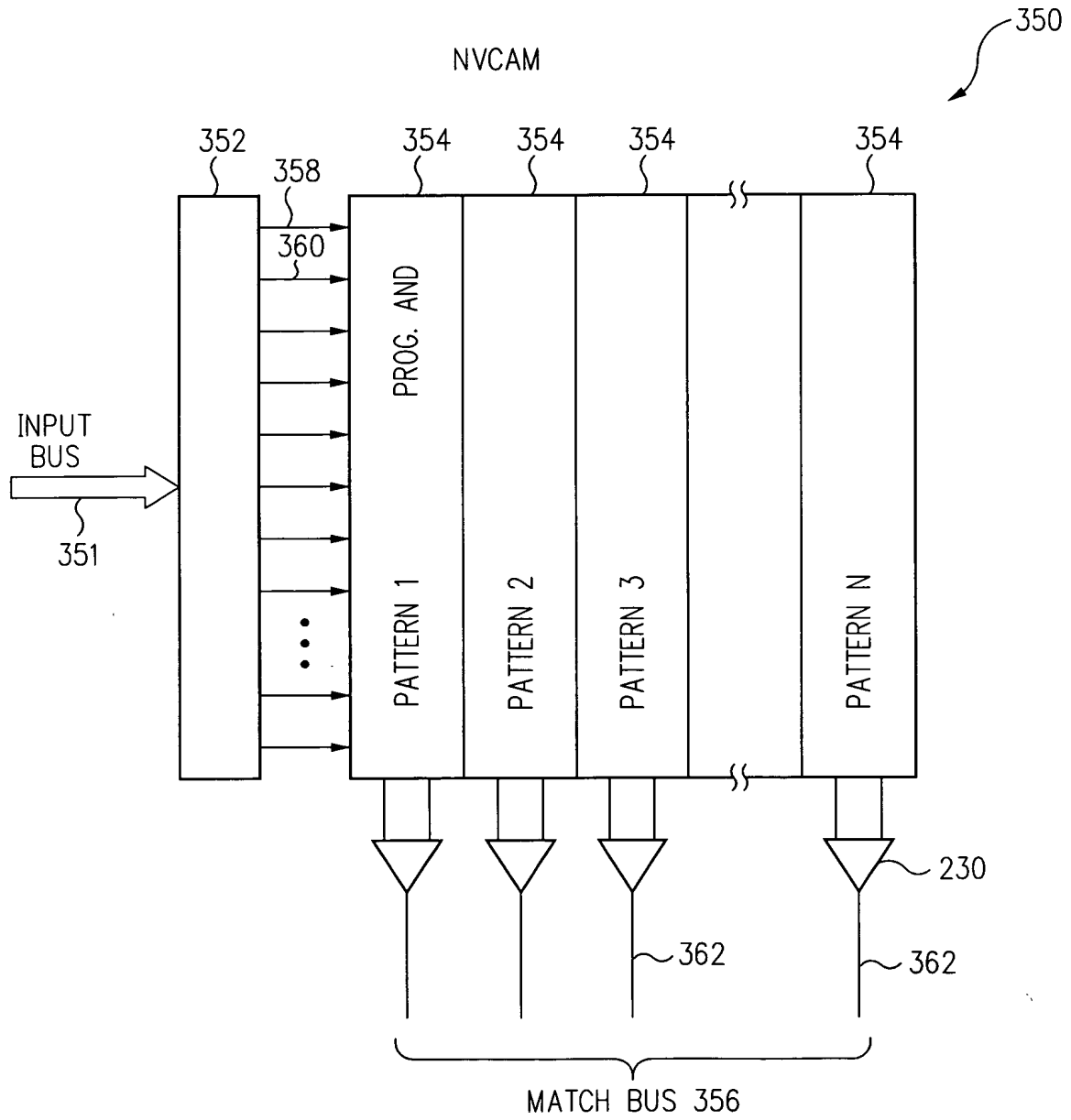


FIG. 10

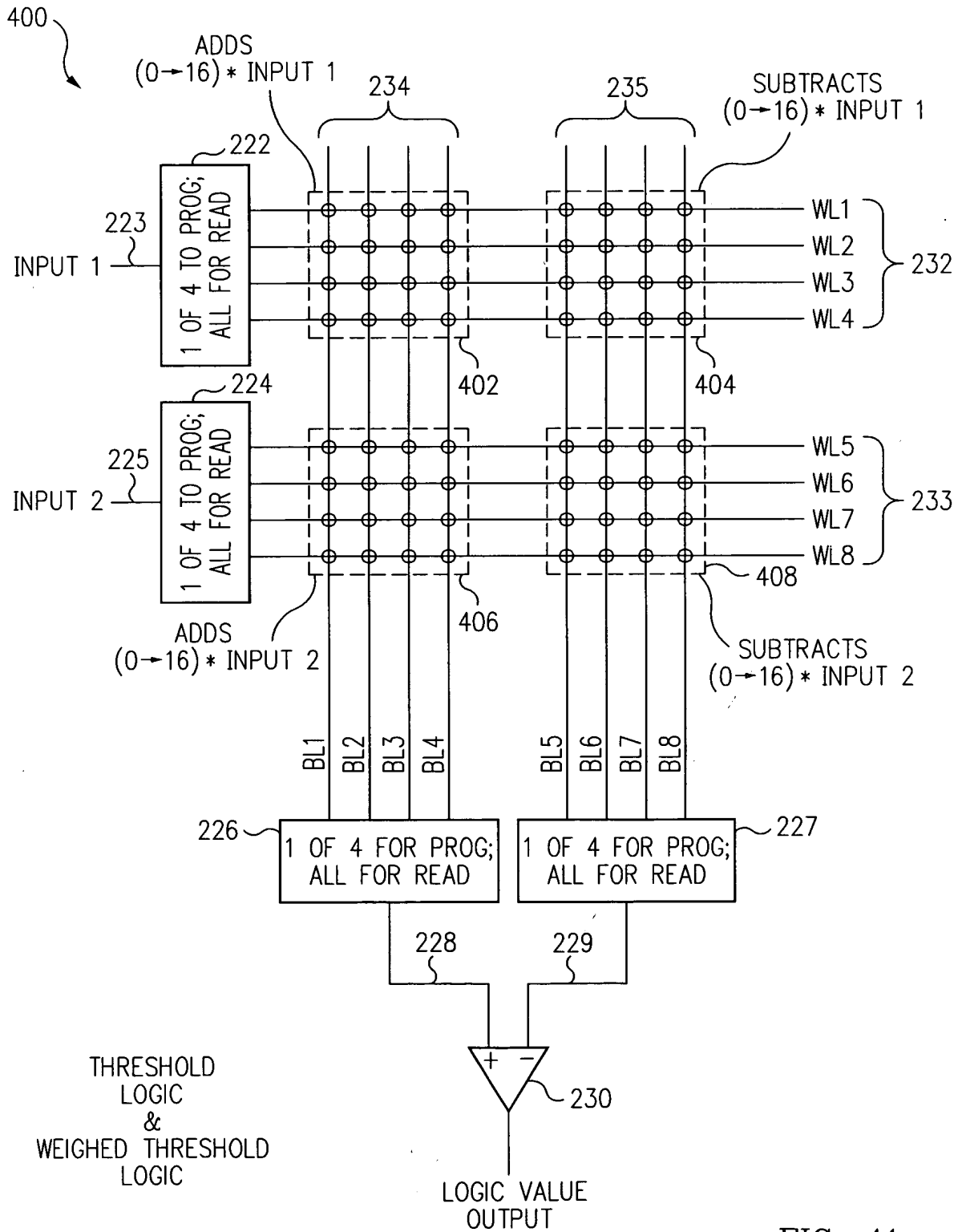


FIG. 11